

PATENT APPLICATION

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Applicant: Steven S. Greenberg
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**INFORMATION DISCLOSURE CITATION
FORM PTO-1449 (Modified)****U.S. PATENT DOCUMENTS**

<u>Exam Init</u>	<u>Ref</u>	<u>Document Number</u>	<u>Issue Date</u>	<u>Name</u>	<u>Class</u>	<u>Sub- Class</u>

FOREIGN PATENT DOCUMENTS

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OTHER DOCUMENTS

<u>Exam Init</u>	<u>Ref</u>	<u>(Including Author, Title, Date, Pertinent Pages, Etc.)</u>
DNL	✓	Ernst G. Ulrich, "NON-INTEGRAL EVENT TIMING FOR DIGITAL LOGIC SIMULATION", 1976, pages 61-68.
DNL	✓	IEEE Computer Society, "IEEE Standard VHDL Analog and Mixed-Signal Extensions", December 23, 1999, pages 118-123 and 173-175 (for reference only; not to be disclosed in IDS).
DNL	✓	Timothy A. Budd, "Classical Data Structures in C++", June 1994, pages 355-379.
DNL	✓	Ernst G. Ulrich, " Exclusive Simulation of Activity in Digital Networks", February 1969, pages 102-110.

Examiner: Dwin Craig
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